



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HIRAOKA, Tetsuya, et al.**

Serial No.: **09/973,002**

Group Art Unit: **2827**

Filed: **October 10, 2001**

Examiner: **Lourdes C. Cruz**

P.T.O. Confirmation No.: 4785

For. **SEMICONDUCTOR DEVICE WITH STACK OF SEMICONDUCTOR CHIPS**

AMENDMENT UNDER 37 CFR §1.111

Commissioner for Patents
Washington, D.C. 20231

October 28, 2002

Sir:

The response to the Office Action dated **June 27, 2002**, is extended to **October 27, 2002** by a 1 month Petition for Extension of Time. October 27, 2002 being a Sunday, therefore, the effective due date is shifted to October 28, 2002. Please amend the above-identified application as follows:

IN THE CLAIMS:

Please **cancel** claim ~~3~~ without prejudice or disclaimer.

Please amend claims 1-2 and 5-6 as indicated below:

1. (Amended) A semiconductor device comprising:
- a substrate having a first plurality of pads and a second plurality of pads;
 - a first semiconductor chip mounted on said substrate and having a third plurality of pads;
 - a plate member arranged on said first semiconductor chip having a first end at an inward position of said first semiconductor chip adjacent from the third plurality of pads, and a second end

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